

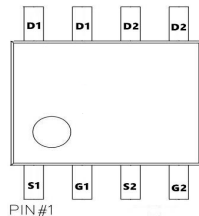
TM06V03S

P+P-Channel Enhancement Mode Mosfet

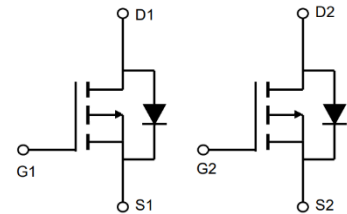
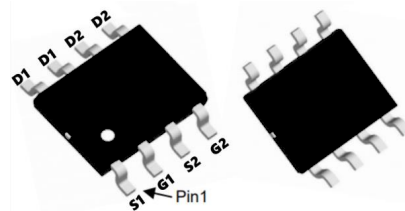
<p>General Description</p> <ul style="list-style-type: none"> • Low $R_{DS(ON)}$ • RoHS and Halogen-Free Compliant <p>Applications</p> <ul style="list-style-type: none"> • Load switch • PWM 	<p>Product Summary</p> <p>$V_{DS} = -30V$ $I_D = -6.0A$</p> <p>$R_{DS(ON)} = 35m\Omega$ (typ.) @ $V_{GS} = -10V$</p> <p>100% UIS Tested 100% R_g Tested</p>
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S: SOP-8L



Marking: 4953A



Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	-5.8	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	-3.8	A
I_{DM}	Pulsed Drain Current ²	-25	A
EAS	Single Pulse Avalanche Energy ³	---	mJ
I_{AS}	Avalanche Current	---	A
$P_D @ T_A = 25^\circ C$	Total Power Dissipation ⁴	2.0	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	100	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	---	$^\circ C/W$

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Electrical Characteristics (T_J=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} = 0V, I _D = -250μA	-30	-	-	V
Gate-body Leakage current	I_{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V _{DS} = -30V, V _{GS} = 0V	-	-	-1	μA
Gate-Threshold Voltage	V_{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1.0	-1.5	-2.1	V
Drain-Source On-Resistance ³	R_{DS(on)}	V _{GS} = -10V, I _D = -4.1A	-	37	46	mΩ
		V _{GS} = -4.5V, I _D = -3A	-	47	62	
Dynamic Characteristics⁴						
Input Capacitance	C_{iss}	V _{DS} = -15V, V _{GS} = 0V, f = 1MHz	-	530	-	pF
Output Capacitance	C_{oss}		-	70	-	
Reverse Transfer Capacitance	C_{rss}		-	56	-	
Switching Characteristics⁴						
Total Gate Charge	Q_g	V _{GS} = -10V, I _D = -4.1A, V _{DS} = -15V	-	10	-	nC
Gate-Source Charge	Q_{gs}		-	2	-	
Gate-Drain Charge	Q_{gd}		-	2.8	-	
Turn-On Delay Time	t_{d(on)}	V _{GS} = -10V, V _{DD} = -15V, ,R _{GEN} = 6Ω, I _D = -4.1A,	-	6.9	-	ns
Rise Time	t_r		-	12	-	
Turn-Off Delay Time	t_{d(off)}		-	19	-	
Fall Time	t_f		-	7.5	-	
Source-Drain Body Diode Characteristics						
Diode Forward Voltage ³	V_{SD}	I _S = -1.7A, V _{GS} = 0V	-	-	-1.2	V
Continuous Source Current	I_S		-	-	-5.8	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C.
2. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse width≤300μs, duty cycle≤2%.
4. This value is guaranteed by design hence it is not included in the production test.

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Typical Characteristics

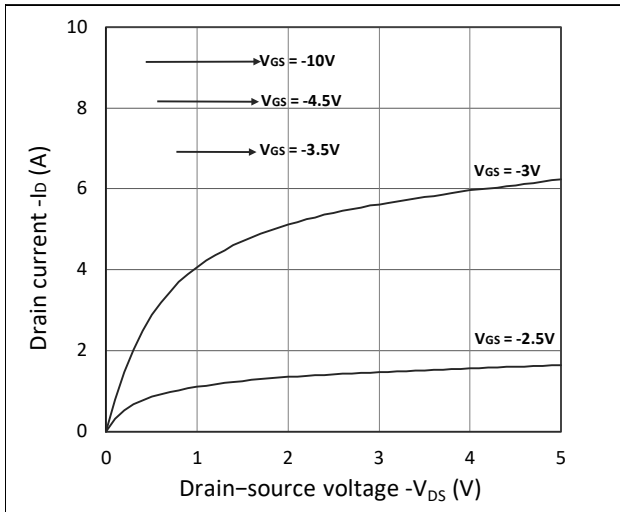


Figure 1. Output Characteristics

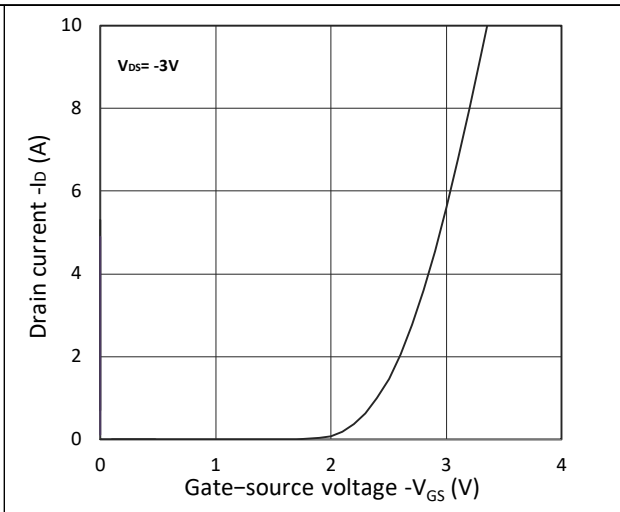


Figure 2. Transfer Characteristics

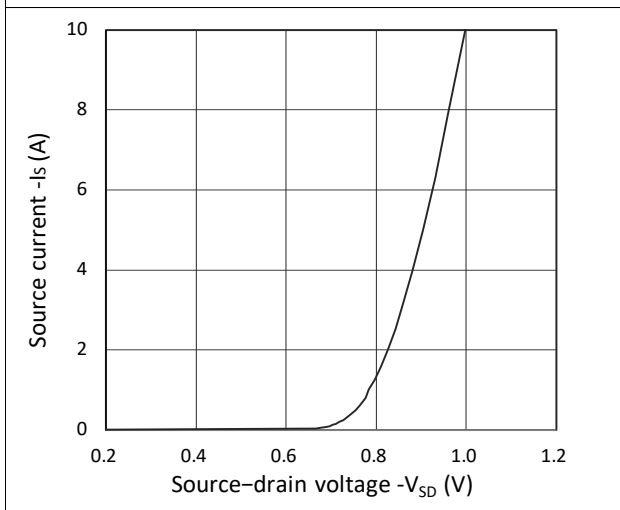


Figure 3. Forward Characteristics of Reverse

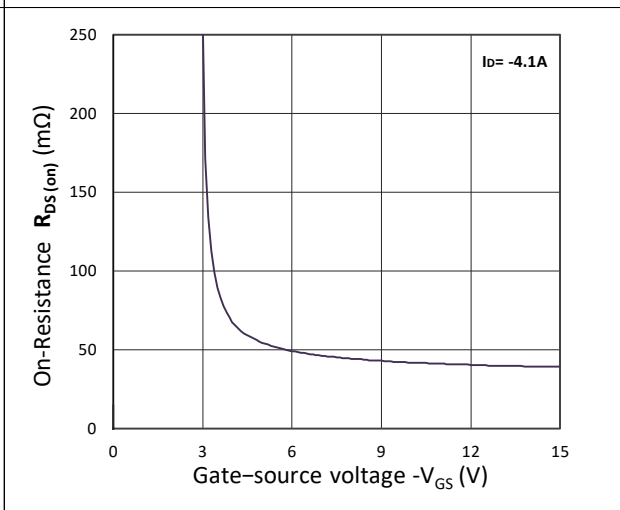


Figure 4. $R_{DS(on)}$ vs. V_{GS}

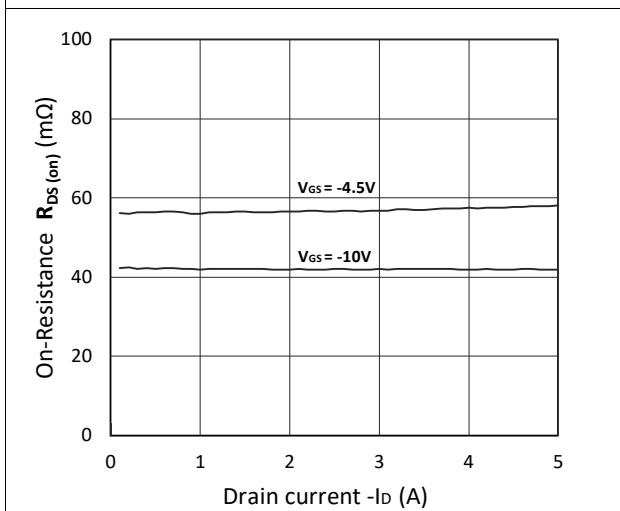


Figure 5. $R_{DS(on)}$ vs. I_D

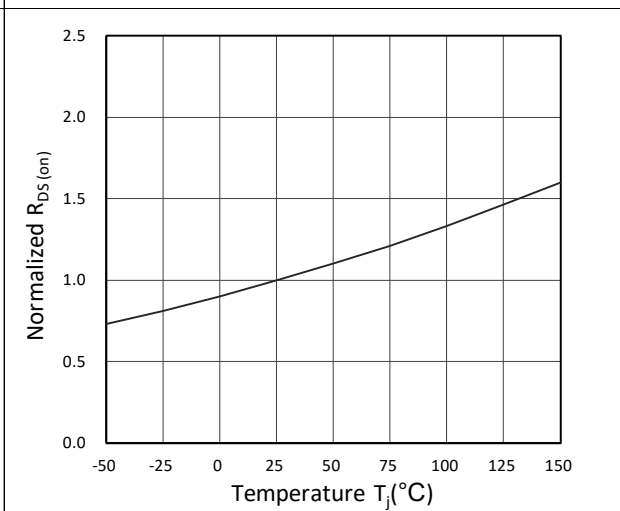


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature



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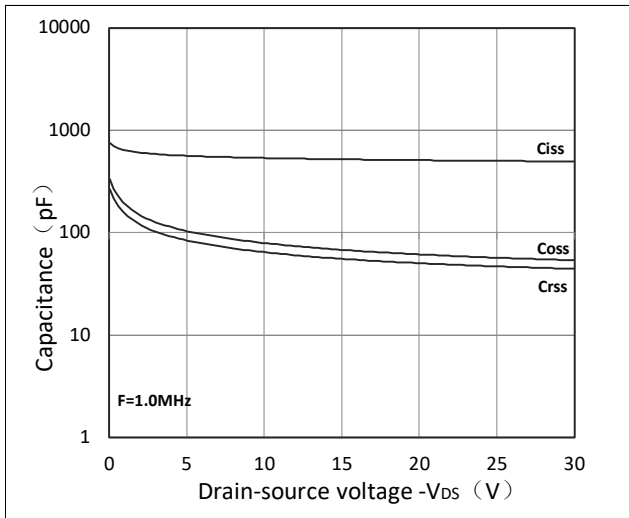


Figure 7. Capacitance Characteristics

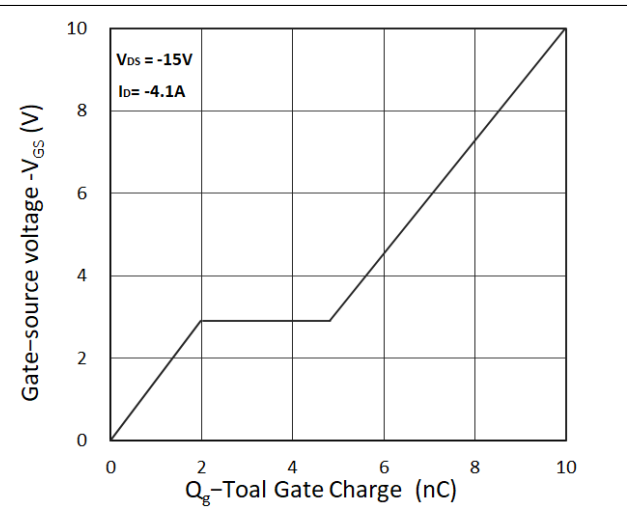
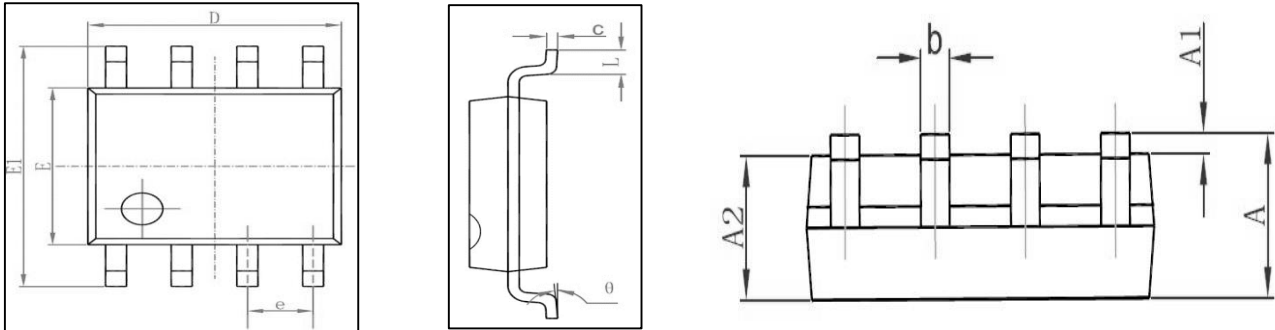
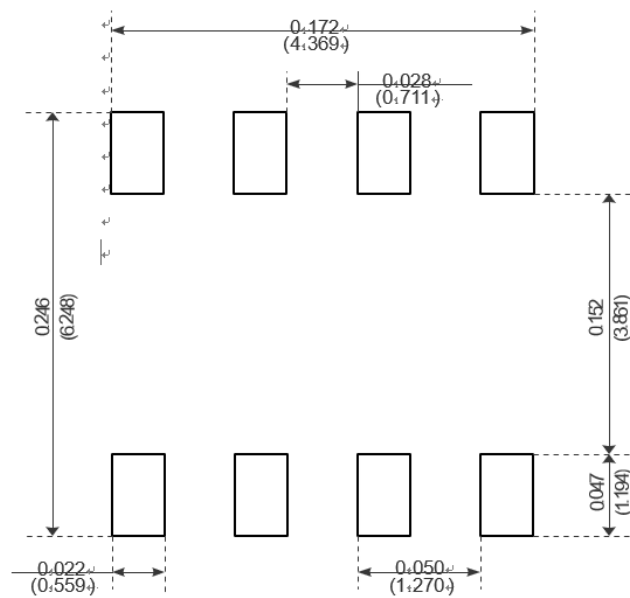


Figure 8. Gate Charge Characteristics

Package Mechanical Data:SOP-8L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Recommended Minimum Pads